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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,673	07/30/2003	Hee Bok Kang	40296-0036	6550
26633	7590 09/20/2005		EXAMINER	
1100001101	IRMAN WHITE & MC	NGUYEN, VAN THU T		
	SISLAND AVE, NW ON, DC 20036-3001	ART UNIT	PAPER NUMBER	
	,		2824	

DATE MAILED: 09/20/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	ı No.	Applicant(s)				
Office Action Summary		10/629,673	ı	KANG, HEE BOK				
		Examiner		Art Unit				
		VanThu Ng		2824				
Period for	The MAILING DATE of this communicated Reply	ition appears on the d	over sheet with the	correspondence ad	ddress			
WHICI - Extens after S - If NO   - Failure Any re	PRTENED STATUTORY PERIOD FOR HEVER IS LONGER, FROM THE MAIL sions of time may be available under the provisions of 3 EIX (6) MONTHS from the mailing date of this community of the maximum statuth is to reply within the set or extended period for reply will ply received by the Office later than three months after it patent term adjustment. See 37 CFR 1.704(b).	LING DATE OF THIS 37 CFR 1.136(a). In no even cation. ory period will apply and will i, by statute, cause the applic	S COMMUNICATIO t, however, may a reply be ti- expire SIX (6) MONTHS from ation to become ABANDON8	N. imely filed in the mailing date of this of ED (35 U.S.C. § 133).	·			
Status								
1)	Responsive to communication(s) filed o	on <i>AF on 7/13/05 an</i>	d RCE on 8/11/05.					
· · · ·	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.							
3) 🗀 🥴								
(	closed in accordance with the practice	under Ex parte Qua	yle, 1935 C.D. 11, 4	53 O.G. 213.				
Dispositio	on of Claims							
4)🖂 (	4)⊠ Claim(s) <u>1-9</u> is/are pending in the application.							
4	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)🛛 (	5) Claim(s) 3-9 is/are allowed.							
6)⊠ (	☑ Claim(s) <u>1-2</u> is/are rejected.							
7) 🗌 (	Claim(s) is/are objected to.							
8) 🗌 (	Claim(s) are subject to restrictio	n and/or election red	ıuirement.					
Application	on Papers							
9)□ T	he specification is objected to by the E	Examiner.						
10)⊠ T	10)⊠ The drawing(s) filed on <u>30 July 2003</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.							
,	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
6	Replacement drawing sheet(s) including the	e correction is required	l if the drawing(s) is ob	bjected to. See 37 C	FR 1.121(d).			
11)□ T	he oath or declaration is objected to by	y the Examiner. Note	e the attached Office	e Action or form P	TO-152.			
Priority u	nder 35 U.S.C. § 119							
	cknowledgment is made of a claim for ☑ All b)☐ Some * c)☐ None of:	foreign priority unde	er 35 U.S.C. § 119(a	a)-(d) or (f).				
•	1.⊠ Certified copies of the priority documents have been received.							
2	2. Certified copies of the priority documents have been received in Application No							
;	B.☐ Copies of the certified copies of t	• •		ed in this National	Stage			
	application from the International	•						
* Se	ee the attached detailed Office action for	or a list of the certifie	ed copies not receive	ed.				
Attachment(	•							
	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-	-948)	l) Interview Summary Paper No(s)/Mail D					
3) 🔯 Inform	ation Disclosure Statement(s) (PTO-1449 or PTo No(s)/Mail Date <u>07/13/2005</u> .	O/SB/08) 5	5) Notice of Informal F 5) Other:		O-152)			

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#### Response to Amendment

1. This Office Action is in response to After Final Amendment filed on July 13, 2005 and Request for Continued Examination filed on August 11, 2005.

- 2. Claims 1-9 are pending.
- 3. Claims 10-20 are cancelled.

# Response to Arguments

4. Applicant's arguments with respect to claims 1-2 have been considered but are moot in view of the new ground(s) of rejection.

### Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. Claims 1-2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Heap (PGPub. 2004/0093457) in view of Gulpta et al. (U.S. Patent No. 6,405,286).

Regarding claims 1-2, Heap discloses an interleave control device using any type of memory device that is organized into blocks or banks (FeRAM fit into this category, see paragraph [0052]), comprising:

a memory array inherently formed in a single chip which controls access time differently in each address and include a plurality of single banks (addresses corresponding to each memory banks in memory array 903 which are operated in an interleave manner, see Fig. 9);

a memory interleave controller (memory controller 901, see Figs. 9-11) for differently controlling a memory interleave operation depending on input physical addresses, and to change an address path of the memory array (i.e. for selecting other banks beside current one); and

a bus configured to transfer data between the memory array and the memory interleave controller (data bus 907, see Fig. 9).

Heap further discloses that the memory interleave controller 901 control the interleave operation by mapping input physical addresses to logical addresses with the use of hardware. However, Heap also mentions that the procedure can be implemented with a processor readable medium using software with programming code essentially to perform the necessary tasks, wherein the processor readable medium (can be called registers) may include any medium that can store information, and nonvolatile ferroelectric memory is one of the kind (see paragraph [0054]).

However, Heap does not disclose that the memory interleaving operation is controlled depending on an access latency time and a restore latency time which are set in a memory interleave region corresponding to lower address bits of row address bits.

Gulpta et al. disclose, in FIG. 2, an interleave control device comprising a memory interleave controller (22, see FIG. 2) configured to program a code using inherent registers (in Interleaving Scheme LookUp Table 24) for differently controlling a memory interleave operation depending on an access latency time (i.e. row access time) and a restore latency time (i.e. prechage time) which are set in a memory interleave region corresponding to lower address bits

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of row address bits in a memory (8 bits for DRAM Page Interleaving within 11 to 14 Row Bits in FIG. 5, also see column 9, lines 3-13).

Since Heap and Gulpta et al. are both from the same field of endeavor, the purpose disclosed by Gulpta et al. would have been recognized in the pertinent art of Heap.

It would have been obvious at the time the invention was made to a person having ordinary skill in the art to control the interleave operation depending on access latency time and restore latency time because that is one of well-known interleaving schemes called Page Interleaving.

# Allowable Subject Matter

7. Claims 3-9 are allowed.

#### Conclusion

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VanThu Nguyen whose telephone number is (571) 272-1881. The examiner can normally be reached on Monday-Friday, 9:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent
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system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

September 12, 2005

VanThu Nguyen Primary Examiner Art Unit 2824